Position: IC Layout Engineer

Location: Taiwan

Job Responsibilities:

- Communicate with engineers to provide solutions on verification area;
- Learning for new verification tool and develop related deck;
- Performing layout of custom analog circuit blocks.

Job Requirements:

- Bachelor Degree or above in Electronic Engineering or equivalent;
- 2 years of work experience in IC Layout Design.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: recruit@solomon-systech.com

All applications will be treated in strict confidence and for recruitment purpose only.